

## AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph on page 7, line 25, to page 8, line 2, with the following amended paragraph:

-- INT 8h is a hardware interrupt with the system clock or system interrupt clock continually interrupting every 54.9 microsecond, i.e. a frequency of 18.2 times/sec. In the interrupt routine of INT 8h, a designer can define or hook an interpose service routine desired in order to perform the ~~motion~~-function defined by the interpose service routine when INT 8h is issued (that is, INT 8h is intercepted by the system clock). --

Please replace the paragraph on page 8, lines 7-8, with the following amended paragraph:

-- Fig. 3 shows a-~~of~~ an embodiment of the FDC method according to the invention. --

Please replace the paragraph on page 8, lines 9-11, with the following amended paragraph:

-- As shown in Fig. 3, ~~when~~ the computer system 10 starts floppy diskette driver access through the operating system (step 300) to access data. --

Please replace the paragraph on page 8, line 28, to page 9, line 4, with the following amended paragraph:

-- Secondly, step 303 performs a conventional floppy diskette service routine (~~step 303~~). The central application configuration calls the respective function of the floppy diskette I/O service routine INT 13. Meanwhile, if a DMA transfer is requested, the flag DMA2START of DMA channel 2 is set to "TRUE" (the system generally adopts the function of DMA channel 2 of DMA mode when accessing data from floppy diskette). --

Please replace the paragraph on page 9, lines 20-27, with the following amended paragraph:

-- ~~In step~~ Step 306, ~~detect~~ detects if a FIFO buffer device exists in the FDC 20 or DMA controller 18 and if it is and-enabled. If not, the computer system 10 issues an error signal (step 309) when the maximum delay time  $T_{\text{delay\_max}}$  is greater than a first specific value (e.g. 20 microseconds) ~~appears~~ (~~step 309~~ 307). If yes, the computer system 10 issues an error signal

when the maximum delay time  $T_{\text{delay\_max}}$  is greater than a second specific value (e.g. 250 microseconds) appears (step ~~309~~ 308). --